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WHAT IS CLAIMED IS:

See what's!
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DP?

1. An emulation device comprising:
a serial scan testability interface having at least
first and second scan paths; and
state machine circuitry connected and responsive to
said second scan path generally operable for emulation
control.
2. The emulation device of claim 1 wherein said
serial scan testability interface includes an instruction
register, and scan path selection circuitry responsive to
said instruction register.
3. The emulation device of claim 2 wherein said
serial scan testability interface includes a separate
state machine connected to control said instruction
register and having a sequence of states responsive to an
externally supplied digital signal.
4. The emulation device of claim 1 wherein said
serial scan testability interface comprises a JTAG
interface.
5. The emulation device of claim 1 wherein said
second scan path includes scan registers for holding
respective emulation command codes.
6. The emulation device of claim 1 wherein said
second scan path includes a scan register for holding
domain locking signals.